



Implementation of Seven Stage Diode Clamped Multi Stage Inverter

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Abstract - Three phase Seven level Diode Clamped Multilevel Inverter (DCMLI) to simulate various modulating techniques for induction motor load. PWM consists of Phase Disposition strategy, Phase Opposition Disposition strategy and Alternate Phase Opposition Disposition strategy. The Total Harmonic Distortion (THD), VRMS (fundamental), crest factor, form factor and distortion factor are evaluated for various modulation indices. Simulation is performed using MATLAB-SIMULINK.

