



Stability Improvement of a Bus System Using Shunt Active Filter under Different Loads

S.Sindhuja^{#1}, Assistant Professor

[#]Department of EEE, Anna University

Bharathiyar Institute of Engineering Women, Deviyakurichi -636 112

¹lathasindhuja@gmail.com

Abstract—The stability of the bus system is enhanced by using the shunt active filters. Shunt active power filters are designed to reduce the harmonic content in the power system which is caused by nonlinear loads. Nonlinear loads contains harmonics which provides the non sinusoidal waveforms. The harmonic content presented in the system are suppressed and the sinusoidal waveforms of current and voltage are restored by passing the compensating current at a bus system using shunt active filter. Active filter consists of a three phase current controlled voltage source inverter with a DC link capacitor across it. Synchronous Reference Frame (SRF) algorithm is used to generate the reference current. Hysteresis current control technique is worn for the generation of firing pulses to the inverter. This system is simulated using MATLAB/Simulink and the results are presented.

Index Terms— Shunt Active Filter, Synchronous Reference Frame (SRF), stability, Hysteresis current control.

I INTRODUCTION

Power system stability is defined as the ability of power system to preserve its steady stability or recover the initial steady state after any deviation of the system's operation. Now a day's power systems are being operated nearer to their stability limits due to economic and environmental reasons. Stability and operation of a power system is now becoming a very important and challenging issue. The stability of the system gets affected by faults, transient, harmonics, distortion in the voltage and voltage.

Now a day's power electronics devices and non-linear elements are used widely in rectification and switchgear applied to various areas of power system. A power system has the levels of harmonics which cannot be ignored by engineers,

planners, energy conservationists and economists due to the existence of non-linear loads.

In addition, power electronic based devices are commonly used for inversion, rectification and other applications. However the Power electronics devices are more efficient, they generate harmonics in the power system. Power systems efficiency consider only distortion-free waveforms, (the voltage and current waveforms should be sinusoidal}. A harmonic is an integral multiple of the fundamental frequency. The major sources of harmonics are the converter used as rectifiers, adjustable speed drives, switched-mode supplies, frequency changers for induction heating in Power systems. As nonlinear loads correspond to an ever-increasing percentage of the total load of a power system, harmonic studies have become an essential part in power systems.

Most of the system prefers nonlinear loads. This result in the reduction of power factor, reduces the efficiency and reduces the system performs. The remedy for these distortions different types of filters is used. Passive filter has some disadvantage such as it produce some resonance problem, it provide only fixed compensation and increased in size. To overcome these drawbacks active filter is used. Active filter is used to compensate the harmonics and increases the total harmonic distortion. It uses active components such as MOSFET, IGBT-transistors etc. both the shunt and series active filter is used. The main aim of this paper is to reduce the current and improve the system stability by used shunt active filter to compensate the harmonic current. Thus the harmonic component present in the waveforms is eliminated and the current waveforms are sinusoidal. Thus the losses in the system are reduced and the reactive power is compensated.

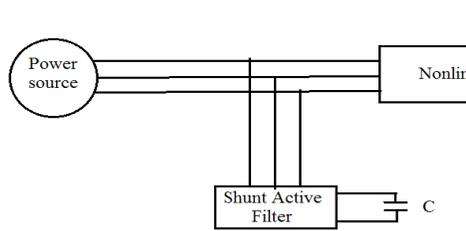


Figure 1: Block diagram of Active Filter

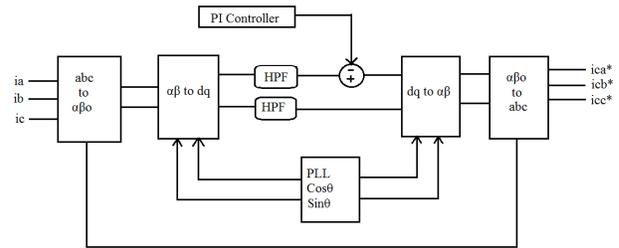


Figure 3: Model of SRF.

Many types of control schemes and controllers are existing. Here SRF algorithm is used for the reference current extraction from the distorted source current. The switching pulses for the power circuit is generated using the Hysteresis Current Control technique and is found to be very effective.

II. IMPLEMENTATION

A. PI CONTROLLER

The combinations of proportional and integral controller are the PI controller. PI controller is essential to increase the speed of the response and to mitigate the steady state error.

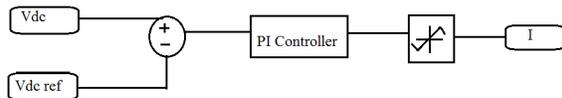


Figure 2: Model of PI controller

To reduce the forced oscillations and to get the maximum value of the reference current PI controller is used. PI controller gives a maximum overshoot and settling time. The above figure shows that the PI controller compares the reference voltage with the actual voltage of the system.

B. SYNCHRONOUS REFERENCE FRAME TECHNIQUE

Synchronous reference frame theory is used to generate the reference current which is based on time domain. This theory is used for its simplicity and it uses only the algebraic calculation, achieve the fast and accurate harmonic extraction from the system. The three-phase reference currents (i_{ca}^* , i_{cb}^* , i_{cc}^*) used by the active power filters is extracted by using Synchronous theory. Figure 3 shows the block diagram which explains three-phase SRF-theory which is used to extract the harmonic components.

Here the source currents (i_a, i_b, i_c) are transformed into two-phase stationary frame ($\alpha\beta$) from the three-phase stationary frame (a-b-c), as per equation (1).

$$\begin{bmatrix} i_\alpha \\ i_\beta \\ i_o \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -1/2 & -1/2 \\ 0 & \sqrt{3}/2 & -\sqrt{3}/2 \\ 1/\sqrt{2} & 1/\sqrt{2} & 1/\sqrt{2} \end{bmatrix} \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} \rightarrow (1)$$

Now, the two phase current quantities i_α and i_β of stationary $\alpha\beta$ -axes are transformed into active and reactive components of synchronous frame (d-q-axes) using equation (2), where $\cos\theta$ and $\sin\theta$ represents the synchronous unit vectors which can be generated using phase-locked loop system (PLL). The phase-locked loop system provides $\cos\theta$ and $\sin\theta$ for synchronism.

$$\begin{bmatrix} i_d \\ i_q \end{bmatrix} = \begin{bmatrix} \cos\theta & \sin\theta \\ -\sin\theta & \cos\theta \end{bmatrix} \begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} \rightarrow (2)$$

High pass filter is used to eliminate the harmonic content present in the system. The d-axis current is a combination of active fundamental current (i_{dc}) and the load harmonic current (i_h). The reference current is obtained from PI controller. By subtracting i_{dc} part from the total d-axis current (i_d), which leaves behind the harmonic component present in the load current AC component can be obtained. The q-axis current is used to calculate the reference compensation currents.

Now the active and reactive current components are changed into two stationary frame components using inverse transformation using equation (3).

$$\begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} = \begin{bmatrix} \cos\theta & -\sin\theta \\ \sin\theta & \cos\theta \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix} \rightarrow (3)$$

Finally the current from two phase stationary frame $\alpha\beta 0$ is transformed back into three-phase stationary frame abc as per equation (4) and the compensation reference currents i_{ca}^* , i_{cb}^* and i_{cc}^* are obtained.

$$\begin{bmatrix} i_{ca}^* \\ i_{cb}^* \\ i_{cc}^* \end{bmatrix} = [T_{abc}] \begin{bmatrix} i_{\alpha} \\ i_{\beta} \\ i_o \end{bmatrix} \rightarrow (4)$$

Where,

$$[T_{abc}] = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & 0 & 1/\sqrt{2} \\ -1/2 & \sqrt{3}/2 & 1/\sqrt{2} \\ -1/2 & -\sqrt{3}/2 & 1/\sqrt{2} \end{bmatrix} \rightarrow (5)$$

C. HYSTERISIS CURRENT CONTROL TECHNIQUE

The filter current I_{fabc} and the reference current i_{abc}^* from the synchronous reference frame theory are compared to acquire the switching pulse for the inverters is done using hysteresis current control. The hysteresis current control method is used due to its good stability, a fast response, good accuracy and simple operation. The error signal is then given to the hysteresis band to attain the switching pulses for the inverter.

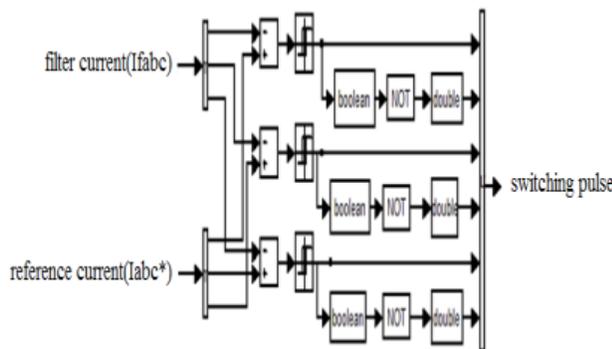


Fig. 4.Hysteresis Current Control

The operation of APF depends on the sequence of pulse generated by the controller. The position between the generated error signals is hysteresis band. When the error signal crosses the upper band, the output voltage changes with

decreasing the input current and when the signal crosses the lower band, the output voltage changes with increase the input current. As the result, switching signals are generated.

The generated switching pulses are given to the power circuit which consists of switching devices with a DC link capacitor. According to the switching pulse induced into the inverter, it generates the compensating current signal. The harmonic current in the line suppressed by giving back the compensating current into the power line.

V .SIMULATION RESULT

5.1. Simulation without Filter

The simulink model of the bus system is shown in the figure 6. The total harmonic distortion present in this system is 10.23%. The supply current is shown in the figure 7.

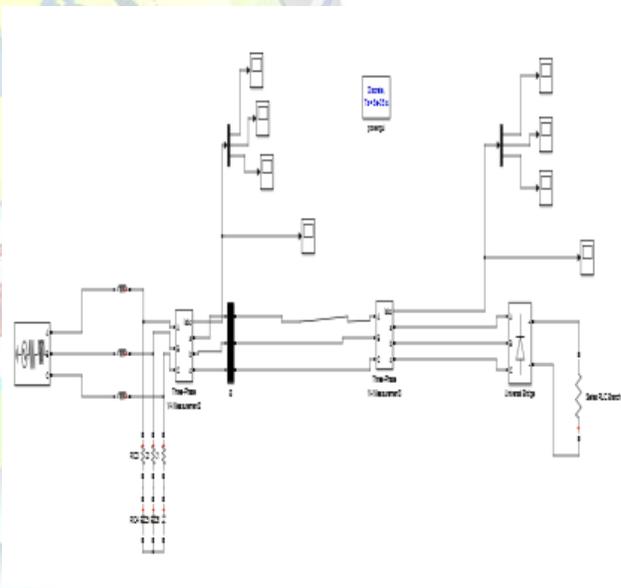


Figure 5: Simulink diagram

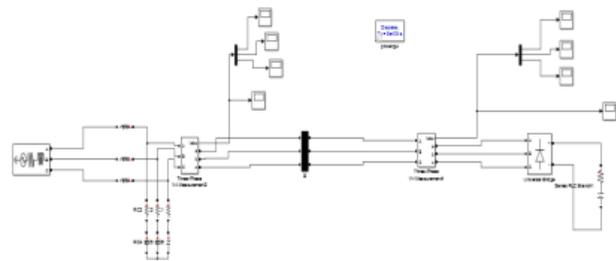


Figure 6: Simulink diagram

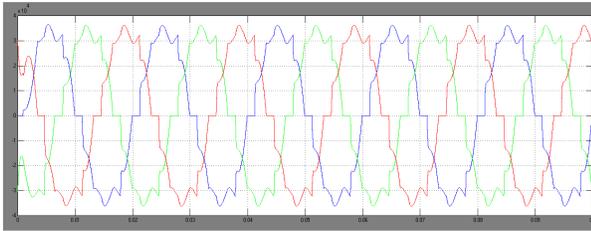


Figure 7: Analysis of supply current.

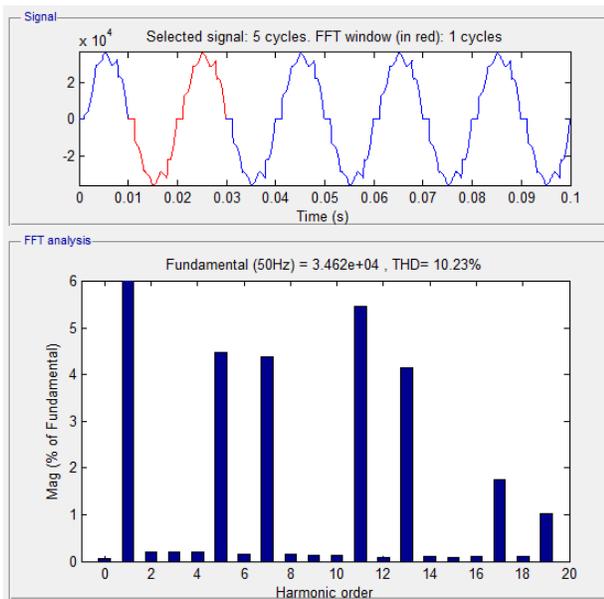


Figure 7: THD analysis.

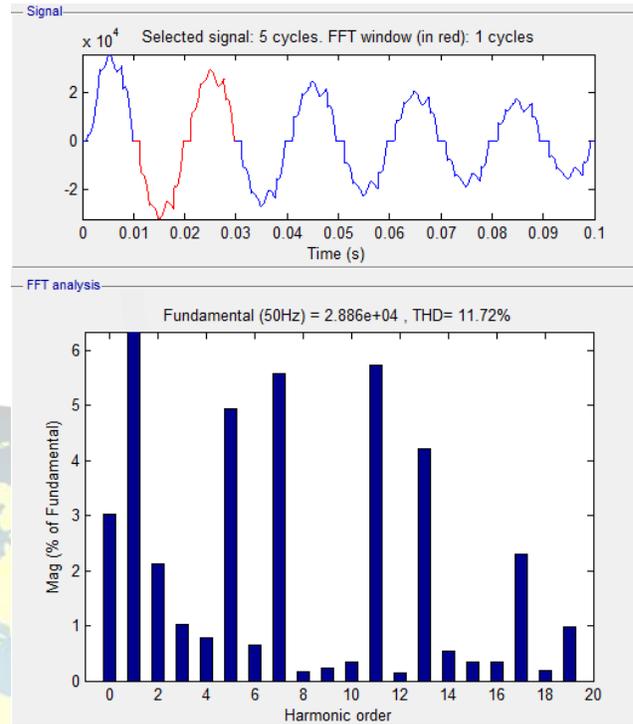


Figure 8: THD analysis.

5.2. Simulation with Filter

The simulink model of the bus system with shunt active filter is shown in the figure 9. The total harmonic distortion of this system is 6.08%. The supply current is shown in the figure 10.

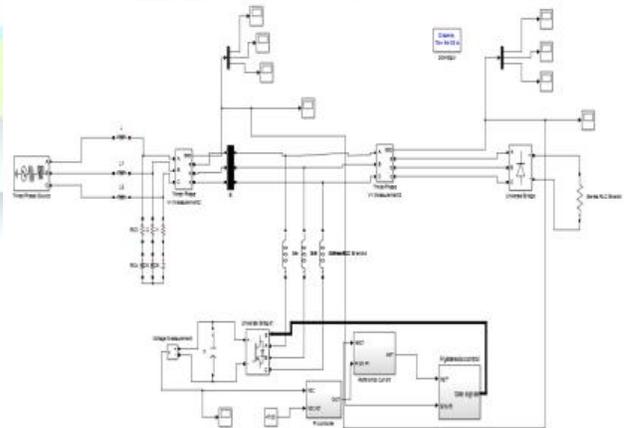


Figure 9: Simulink Model.

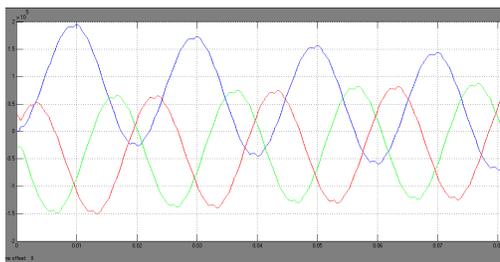


Figure 10: Analysis of supply current.

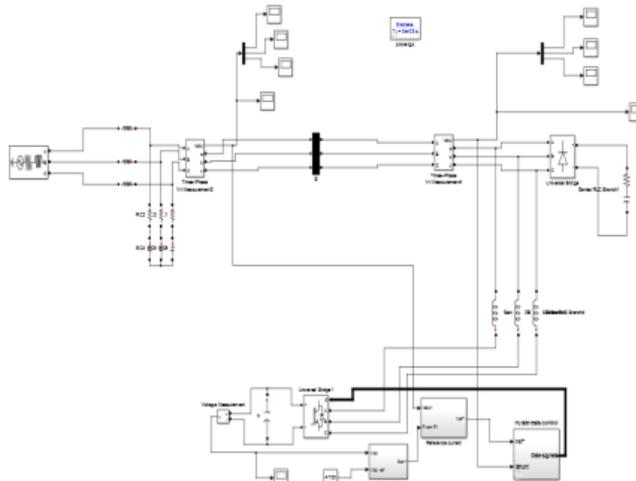


Figure 12: Simulink Model.

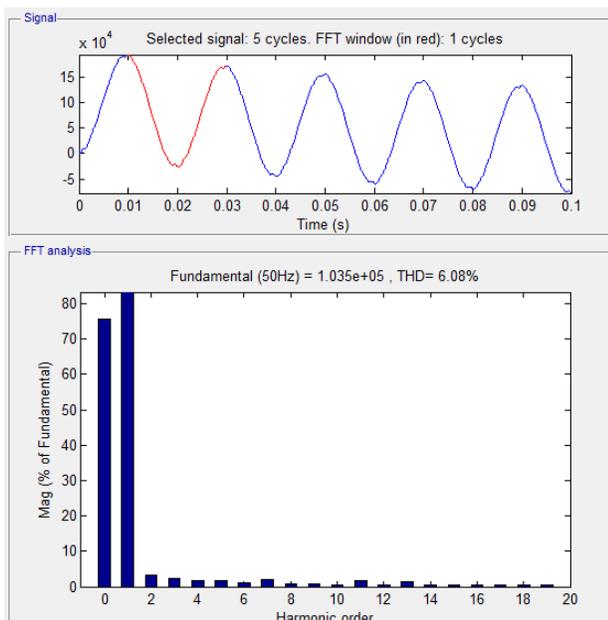


Figure 11: THD analysis

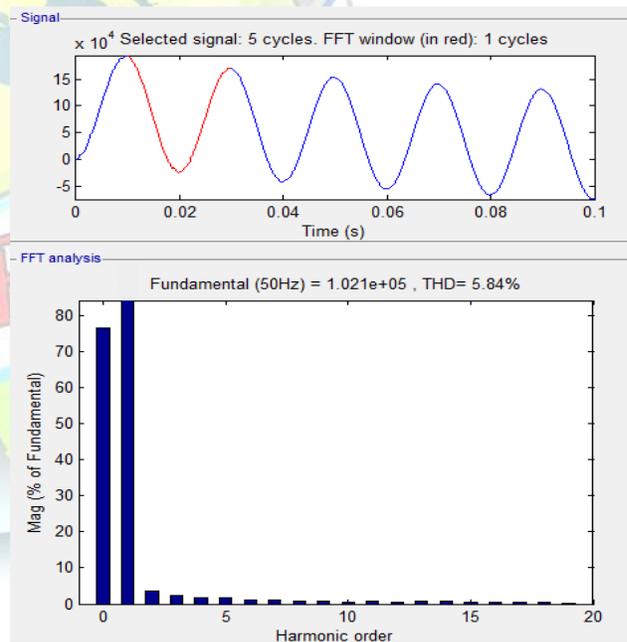


Figure 13: THD analysis

5.3. Performance comparison

NON LINEAR LOADS	WITHOUT FILTER(THD)	WITH FLTER(THD)
------------------	---------------------	-----------------



A	10.23%	6.08%
B	11.72%	5.84%

VI. CONCLUSION

The shunt active filter was designed to eliminate the harmonic content produced by the nonlinear load and produces the sinusoidal. The control techniques such as synchronous reference frame theory and the hysteresis current control techniques were found to be very effective. Then the total harmonic distortion without filter and with filter was compared. The active filter with the control techniques are simulated using MATLAB. Finally the system performance, stability of the system and reduction of harmonic components were achieved.

REFERENCE

- [1] P. Kundur, J. Paserba, V. Ajjarapu, G. Andersson, A. Bose, C. Canizares, N. Hatziaargyriou, D. Hill, A. Stankovic, C. Taylor, T.V. Cutsem, and V. Vittal, "Definition and classification of power system stability: IEEE/CIGRE joint task force on stability terms and definitions," *IEEE Trans. Power Syst.*, vol. 19, no. 3, pp. 1387–1401, Aug. 2004.
- [2] P. Kundur, *Power System Stability and Control*. New York, NY, USA: McGraw-Hill, 1994.
- [3] V. Mahajan, "Power System Stability Improvement with Flexible A.C. Transmission System (FACTS) Controller," *Power System Technology an IEEE Power India Conference, POWERCON 2008*.
- [4] J. Miret, M. Castilla, J. Matas, J. M. Guerrero, and J. C. Vasquez, "Selective harmonic-compensation control for single-phase active power filter with high harmonic rejection," *IEEE Trans. Ind. Electron.*, Vol. 56, No. 8, pp. 3117-3127, Aug. 2009.
- [5] V. Khadkikar, A. Chandra, and B. N. Singh, "Generalised single phase p-q theory for active power filtering: Simulation and DSP based experimental investigation," *IET Power Electron.*, Vol. 2, No. 1, pp. 67-78, Jan. 2009.
- [6] M. Cirrincione, M. Pucci, and G. Vitale, "A single-phase DG generation unit with shunt active power filter capability

by adaptive neural filtering," *IEEE Trans. Ind. Electron.*, Vol. 55, No. 5, pp. 2093-2110, May 2008.

[7] M. Saitou, N. Matsui, and T. Shimizu, "A control strategy of single phase active filter using a novel d-q transformation," in *Proc. IEEE IAS '03*, pp. 1222-1227, 2003.

[8] P. Rodriguez, J. Pou, J. Bergas, J.I. Candela, R. P. Burgos, and D. Boroyevich, "Decoupled double synchronous reference frame PLL for power converters control," *IEEE Trans. Power Electron.*, Vol. 22, No. 2, pp. 584-592, Mar. 2007.

[9] G.D.Marques, V.Fernao Pires, Mariusz Mlinowski, and Marian Kazmierkowski, "An improved synchronous Reference Method for active filters," the International conference on computer as a tool, EUROCON 2007, Warsaw, September - 2007, pp. 2564-2569.

[10] V.Soares,P.Verdelho,G.D.Marques," An instantaneous active reactive current component method for active filters" *IEEE Trans. Power Electronics*, vol. 15, no. 4, July- 2000, pp. 660–669.

[11] M. El-Habrouk, M. K. Darwish, and P. Mehta, —Active power filters: A review, || *IEE Electr. Power Appl.*, vol. 147, no. 5, pp. 403–413, Sep. 2000.

[12] G. Gurusamy and P.M. Balasubramaniam, "Evaluation and Implementation of Three Phase Shunt Active Power Filter for Power Quality Improvement", *IJCE*.Vol. 5, No. 7 -2012.

[13] Y. Han, and L. Xu, "Design and implementation of a robust predictive control scheme for active power filters," *Journal of Power Electronics*, Vol. 11, No. 5, pp. 751-758, Sep. 2011.

[14] J. H. Lee, J. K. Jeong, B. M. Hany, and B. Y. Bae, "New reference generation for a single-phase active power filter to improve steady state performance," *Journal of Power Electronics*, Vol. 10, No. 4, pp. 412-418, Jul. 2010.

[15] H. H. Tumbelaka, L. J. Borle, C. V. Nayar, and S. R. Lee, "A grid current-controlling shunt active power filter," *Journal of Power Electronics*, Vol. 9, No. 3, pp. 365-376, May 2009.