

PFC CONTROLLER FOR DC APPLICATION USING SEPIC CONVERTER

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ABSTRACT—SEPIC converter with a new active snubber cell is proposed. This snubber circuit focused on high frequency AC-DC converter based provides ZVT turn on and ZVS turn off for the main PFC circuits. Different approaches have been switch. Also the auxiliary switches turn on with ZCS proposed to improve efficiency and quality of energy and turn off with ZVS this snubber cell. SS operation by using PFC. conditions are maintained at very wide line and load range and no semiconductor component has an additional voltage stress. In order to solve the problems of the boost converter operating in CCM, a lot of papers have been proposed in the literature. Although these studies are successful and provide most of the desired properties, they still have some drawbacks. In this study the operating principle of the new ZVT PWM DC-DC MOSFET is used as a power switch, discharge loss of the parasitic capacitor becomes important in is piece simulation software for a 1kw and 100 KHZ basic ZVT technique providing the recovery of parasitic capacitor energy an anti-parallel diode to the main switch, an auxiliary switch and an inductor are used for the aim of active suppression. In this circuit, main switch is turned-on perfectly with ZVT and main diode is turned off with ZCS.

I. INTRODUCTION

Energy consumption has been increasing by the effect of technological developments and rising prosperity, therefore energy should be used more efficiently and economical. The increasing nonlinear loads draw harmonic currents which causes failures and corruptions on sensitive devices which connected to the grid. Thus, energy should be used in a quality manner, too. There are international mandatory standards about power factor and harmonics in terms of the use of energy with high quality and efficiency.

II. EXISTING SYSTEM

A new SEPIC converter with an active snubber cell is proposed. The active snubber cell provides main switch to turn ON with zero-voltage transition (ZVT) and to turn OFF with zero-current transition (ZCT).

The converter incorporating this snubber cell can operate with soft switching at high frequencies.

Also, in this converter all semiconductor devices operate with soft switching. There is no additional voltage stress across the main and auxiliary components. Power Factor Correction (PFC) circuits. Power factor can be improved by means of bulk passive filter or very complex and expensive active filters,

DRAWBACKS

High current is required by the equipment due to which the economic cost of the equipment increases.

At power factor correction the current is high which gives rise to high copper losses in the system.

Therefore the efficiency of system is reduced

IV . BLOCK DIAGRAM

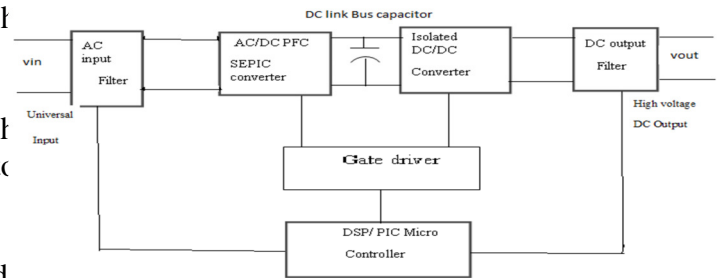


Fig: Block Diagram

III . PROPOSED SYSTEM

A new SEPIC converter with an active snubber cell is proposed, in the proposed circuit, & connected between the ac mains and the rectifier input stage of the switching power supply. All the input ac an effective filter should therefore attenuate all the higher frequencies and only let the mains 50Hz or 60 Hz pass through to the next stage.

However to reduce the current stress on the auxiliary switch a transformer with a high magnetizing inductance is used and so the auxiliary switch turns off hardly, under this magnetizing current. Voltage must pass through the filter before reaching the rectifier. The input line filters are incorporated in most switched mode power supplies to reduce the interference from the electromagnetic and other electrical noises present in the ac lines.

Also a high valued capacitor and a resistance is used to reset the magnetizing energy and the auxiliary switch and the auxiliary diode has additional voltage stress.

Most of the SS energy stored in the snubber inductance is transferred to the output through transformer and so current stress of the auxiliary switch is also reduced. Preventing the EMI signals generated within the power supply from reaching the input ac power line and affecting other equipment connected on the same line. Preventing high frequency voltage and EMI on the power line from passing through and reaching the supply's output. The design and component selection of the input filter is important in ensuring that it does not unnecessarily increase the volume and cost of the supply or compromise the power supply performance. Even though there are various filter designs with different characteristics and effects on power supply performance, the passive L-C filter achieves both the filter functions above while still offering the best balance between size, cost and performance. However, passive filters may introduce undesirable effects, it is therefore important to understand the load and use the appropriate filter design. The L-C passive filters may further be classified according to the design and characteristics. The common types include the

ADVANTAGES

Reduced demand charges.

Increased load carrying capabilities in existing circuit.

Improve voltage. Reduce power system loss.

Reduce carbon foot print.

undamped LC filter, parallel damped filter and series bridge rectifier is a full-wave rectifier. One advantage of a bridge rectifier over a conventional full-wave rectifier is that with a given transformer the bridge rectifier produces a voltage output that is nearly twice that of the conventional full-wave.

TRANSFORMER

The potential transformer will step down the power supply voltage (0-230) to (0-6v) level. The secondary of the potential transformer will be connected to the precision rectifier, which will help result of the small voltage drop across the diode. In op-amp. The advantages of using precision rectifier are it will give peak voltage output as DC; rest of the voltage that can be rectified is the full secondary circuits will give only RMS output.

BRIDGE RECTIFIER

When four diodes are connected as shown in figure, the circuit is called as bridge rectifier. The input to the circuit is applied to the network, and the output is taken from the remaining two corners. Let us assume that the transformer is working properly and there is a positive potential, at point A and negative potential at point B. The positive potential at point A will forward bias D3 and reverse bias D4. The negative potential at point B will forward bias D1 and reverse bias D2. At this time D3 and D1 are forward biased and will allow current flow to pass through the secondary of the transformer back to point B. This path is indicated by the solid arrows. (1) and (2) can be observed across D1 and D2 input terminal, a regulated dc output voltage, V_o , form a second terminal, with the third terminal connected to ground.

The series 78 regulators can be selected for positive regulated voltage from 5 to 24 volts. Similarly, series 79 regulator provides fixed negative regulated voltage from 5 to 24 volts. The regulators can be selected for operation with load currents from 100mA to 1A. The current flow through RL is always in the same direction. In flowing through RL this current develops a voltage corresponding to that shown in waveform (5). Since current flows through the load (RL) during both half cycles of the applied voltage, the bridge rectifier circuit produces a higher output voltage than the conventional full-wave rectifier circuit.

IC VOLTAGE REGULATORS

Voltage regulator comprises a class of widely used ICs. Regulator IC units contain the circuitry for reference source, comparator, amplifier, control device, and overload protection all in a single IC. IC units provide regulation of either a fixed negative voltage, or an adjustably set voltage. The regulator can be selected for operation with load currents from 100mA to 1A. The current flow through RL is always in the same direction. In flowing through RL this current develops a voltage corresponding to that shown in waveform (5). Since current flows through the load (RL) during both half cycles of the applied voltage, the bridge rectifier circuit produces a higher output voltage than the conventional full-wave rectifier circuit.

DC-AC SEPIC CONVERTER

In DC-DC switching converters of the single ended primary inductance (SEPIC) type, using a

high frequency transformer to isolate the load from the AC power line is difficult because its leakage inductance causes serious circuit problems. In the DC-DC converter. The simplest example of an invention, an additional coupling capacitor provides the required DC isolation without affecting circuit performance. The total coupling capacitance may be chosen to limit the power line frequency leakage current to a safe value.

DISCRIPTION

FIELD OF THE INVENTION This invention pertains to DC-DC switching power supplies and, in particular, to obtaining power line isolation between input and output in SEPIC power supply.

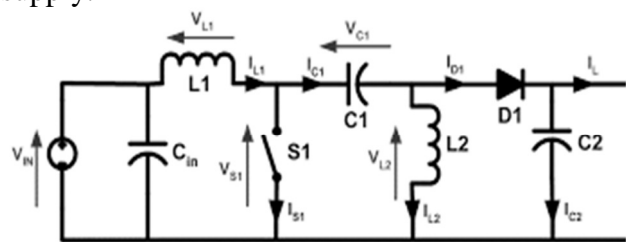


Fig: Sepic converter

A very common need in electronics is converting AC power line energy to DC power to supply an electronic circuit as a load. In addition, it is often necessary to regulate the DC power: that is to maintain the load voltage approximately constant in spite of variations in the power line voltage and the load current. Series regulators--in effect, controllable resistances in series with the load--have given place, in many applications, to switching regulators. In these regulators, the power line voltage is rectified to DC, which is then switched into various inductors and capacitors at a frequency hundreds or thousands of times higher than the power line frequency. These reactance alternately absorb power line energy and deliver it to the load in a manner which is controlled to provide a constant load voltage.

A rectifier can take the shape of several different physical forms such as solid-state diodes, vacuum tube diodes, mercury arc valves, silicon-controlled rectifiers and various other silicon-based semiconductor switches. Rectifiers are used in various devices, including:

- DC power supplies
- Radio signals or detectors
- A source of power instead of generating current
- High-voltage direct current power transmission systems
- Several household appliances use power rectifiers to create power, like notebooks or laptops, video game systems and televisions.

Almost all rectifiers contain more than one diode in particular arrangements. A rectifier also has different waveforms, such as:

- Half Wave: Either the positive or negative wave is passed through or the other wave is blocked. It is not efficient because only half of the input wave form reaches the output.
- Full Wave: Reverses the negative part of the AC wave form and combines it with the positive

V. CIRCUIT DIAGRAM:

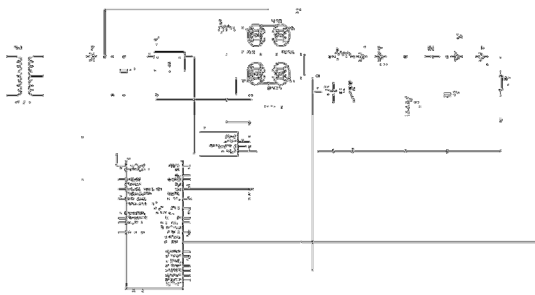


Fig: Circuit diagram

POWER SUPPLY

The present chapter introduces the operation of power supply circuits built using filters, rectifiers and then voltage regulators. Starting with an ac voltage, a steady dc voltage is obtained by rectifying the ac voltage, then filtering to a dc level, and finally, regulating to obtain a desired fixed dc voltage, the regulation is usually obtained from a IC voltage regulator unit, which takes a dc voltage and provides a somewhat lower dc voltage, which remains the same even if the input dc voltage varies or the output load connected to the dc voltage changes. A block diagram containing the parts of typical power supply and the voltage at various points in the unit is shown in fig. the ac voltage typically 120 v RMS, is connected to a transformer which steps that ac voltage down to the level for the desired dc output. A diode rectifier then provides full-wave rectified voltage that is initially filtered by simple capacitor filter to produce a dc voltage. this resulting dc voltage usually has some ripple or ac voltage variation. A regulator circuit can use this dc input to provide a dc voltage that not only has much less ripple voltage but also remains the same dc value even if the input dc voltage varies somewhat, or the load connected to the output dc voltage changes. This voltage regulation is usually

obtained using one of a number of popular voltage regulators IC

TRANSFORMER

The potential transformer will step down the power supply voltage (0-230) to (0-6v) level. Then the secondary of the potential transformer will be connected to the precision rectifier, which the help of op-amp. The advantages of using precision rectifier are it will give peak voltage output as DC; rest of the circuits will give only RMS output.

Isolated DC-DC Converter

This “isolation” refers to the existence of an electrical barrier between the input and output of the DC-DC converter. The simplest example of a non isolated “converter” is the popular LM317 three terminal linear regulators. One terminal for unregulated input, one for the regulated output and one for the common

VI. SEPIC CONVERTER

In DC-DC switching converters of the single-ended primary inductance (SEPIC) type, using a high frequency transformer to isolate the load from the AC power line is difficult because its leakage inductance causes serious circuit problems. In the invention, an additional coupling capacitor provides the required DC isolation without affecting circuit performance. The total coupling capacitance may be chosen to limit the power line frequency leakage current to a safe value.

GATE DRIVER

A gate driver is a power amplifier that accepts a low-power input from a controller IC and produces a high-current drive input for the gate of a high-power transistor such as an IGBT or power MOSFET. Gate drivers can be provided either on-chip or as a discrete module.

MICROCONTROLLER

A microcontroller is a complete microprocessor system built on a single IC. Microcontrollers were developed to meet a need for microprocessor to be put into low cost products. Building a complete microprocessor system on a single chip substantially reduces the cost of building simple products, which use the microprocessor's power to implement their function, because the microprocessor is a natural way to implement many products. This means the idea of using a microprocessor for low cost products comes up often. But the typical 8-bit microprocessor based system, such as one using a Z80 and 8085 is expensive. Both Z80 and 8085 system need some additional circuits to make a microprocessor

VII. CONCLUSION

In this paper, a sepic converter is presented for PFC applications. In the proposed new converter, the main switch is turned-on perfectly with ZVT and turned-off under ZVS. The auxiliary switch is turned-on under ZCS and turned-off with ZVS. All diodes are operating under SS. By using a transformer, during ZVT operation the switching energies are transferred to the output, and so the current stresses of the auxiliary components are significantly decreased. Also, this transformer ensures the usage of sufficient capacitors for ZV turning off of the main and auxiliary switches. The main switch and the main diode are not subjected to any additional voltage and current stresses.

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